

Fig. 1
(Prior Art)

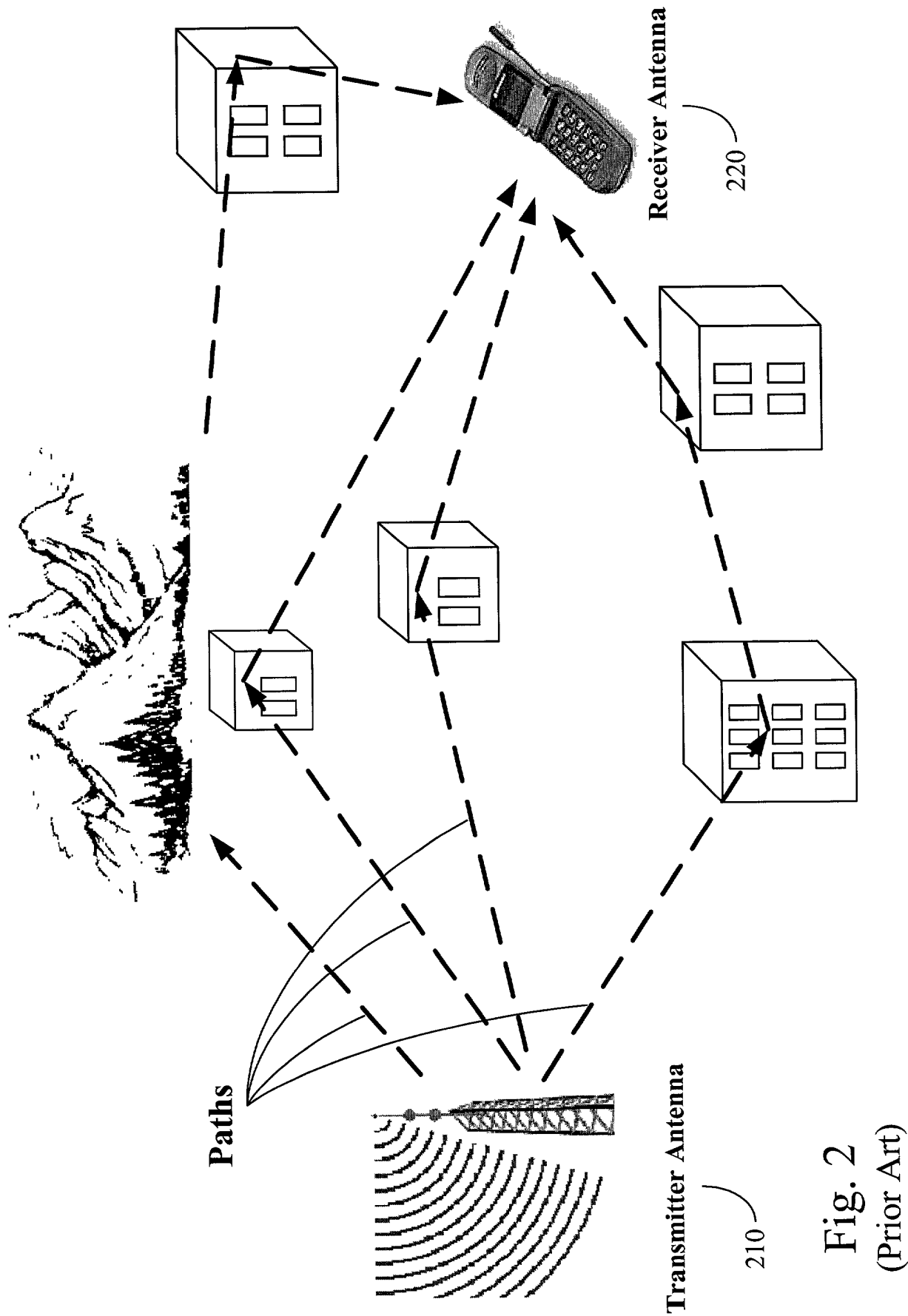


Fig. 2
(Prior Art)

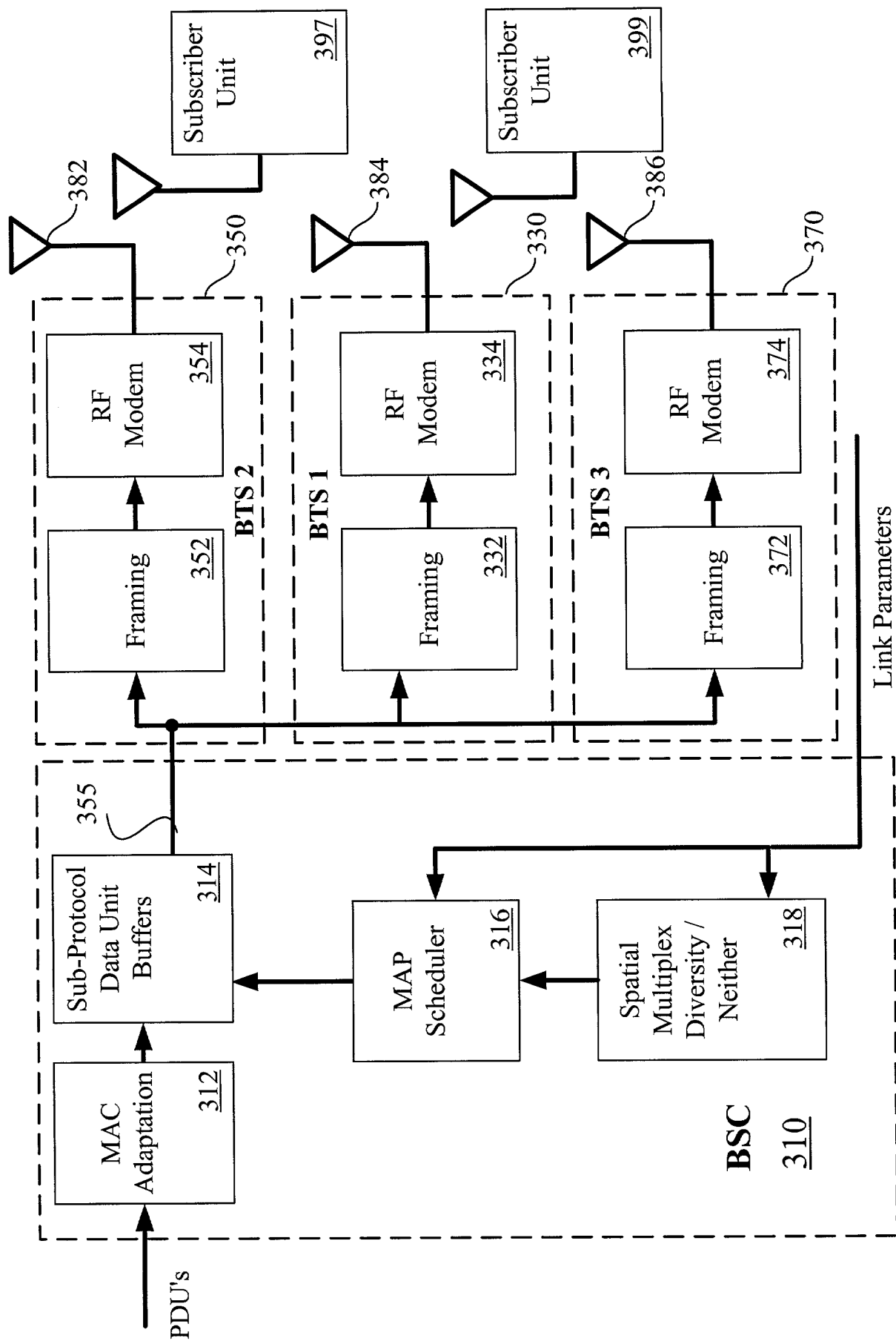


Fig. 3

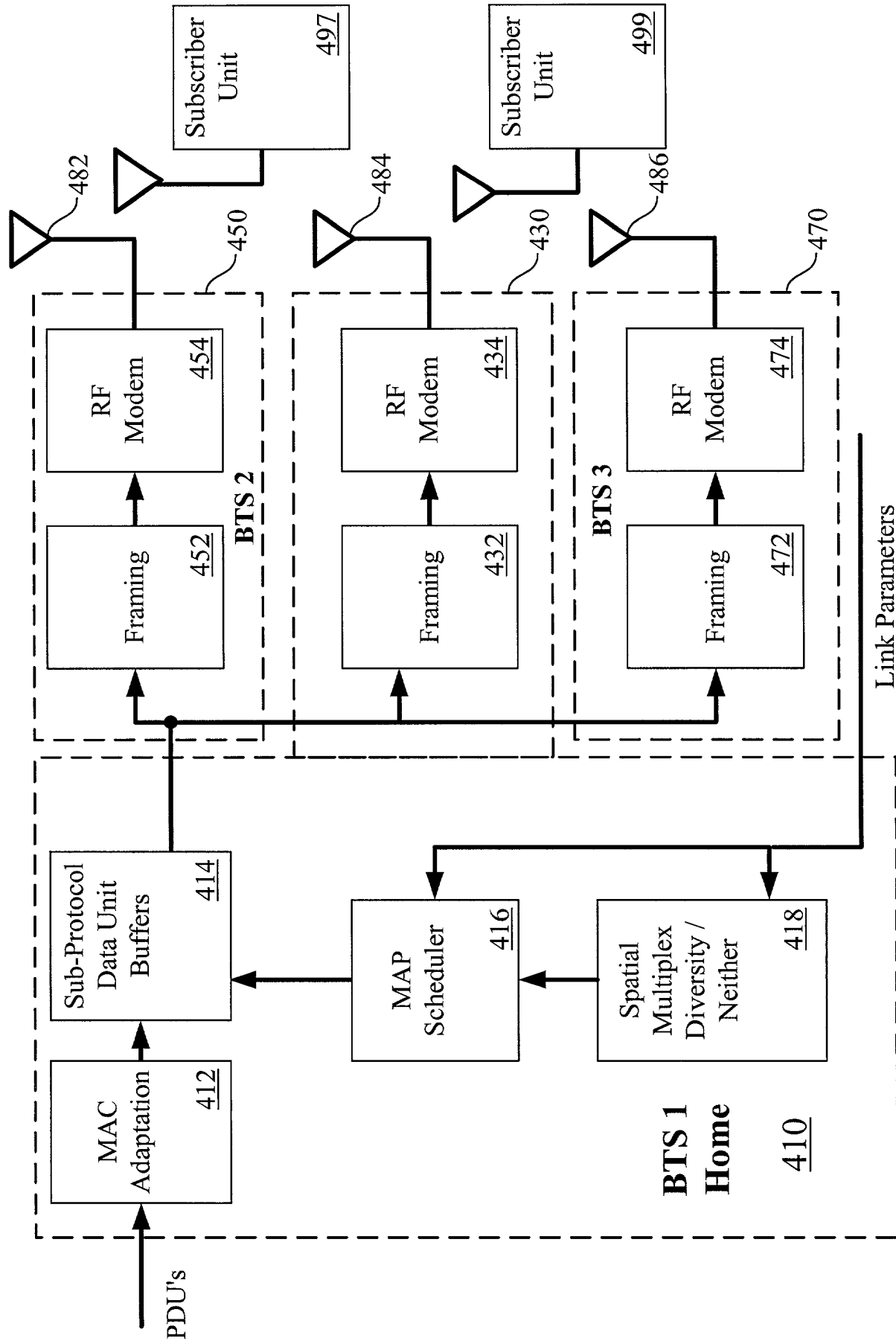


Fig. 4

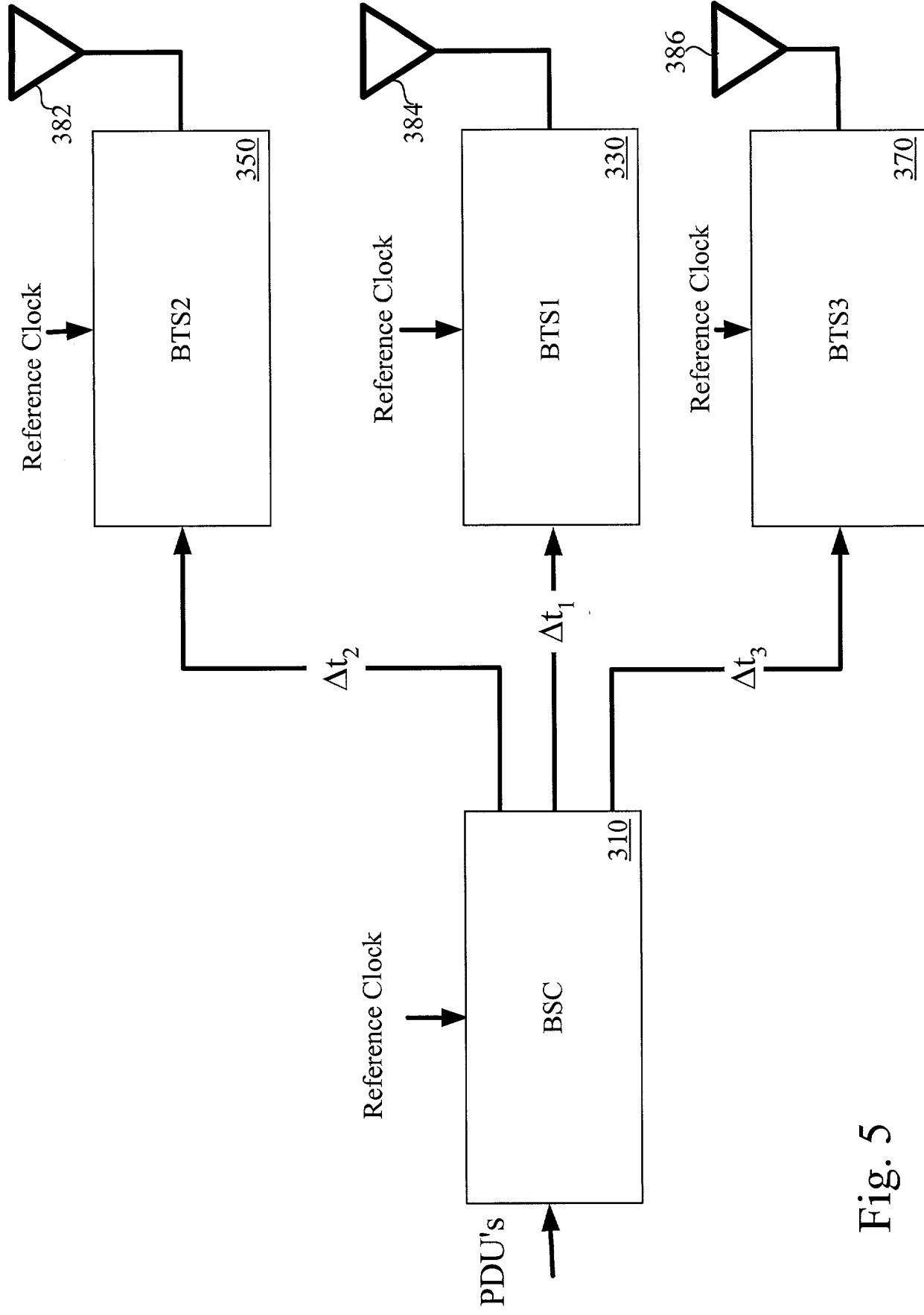


Fig. 5

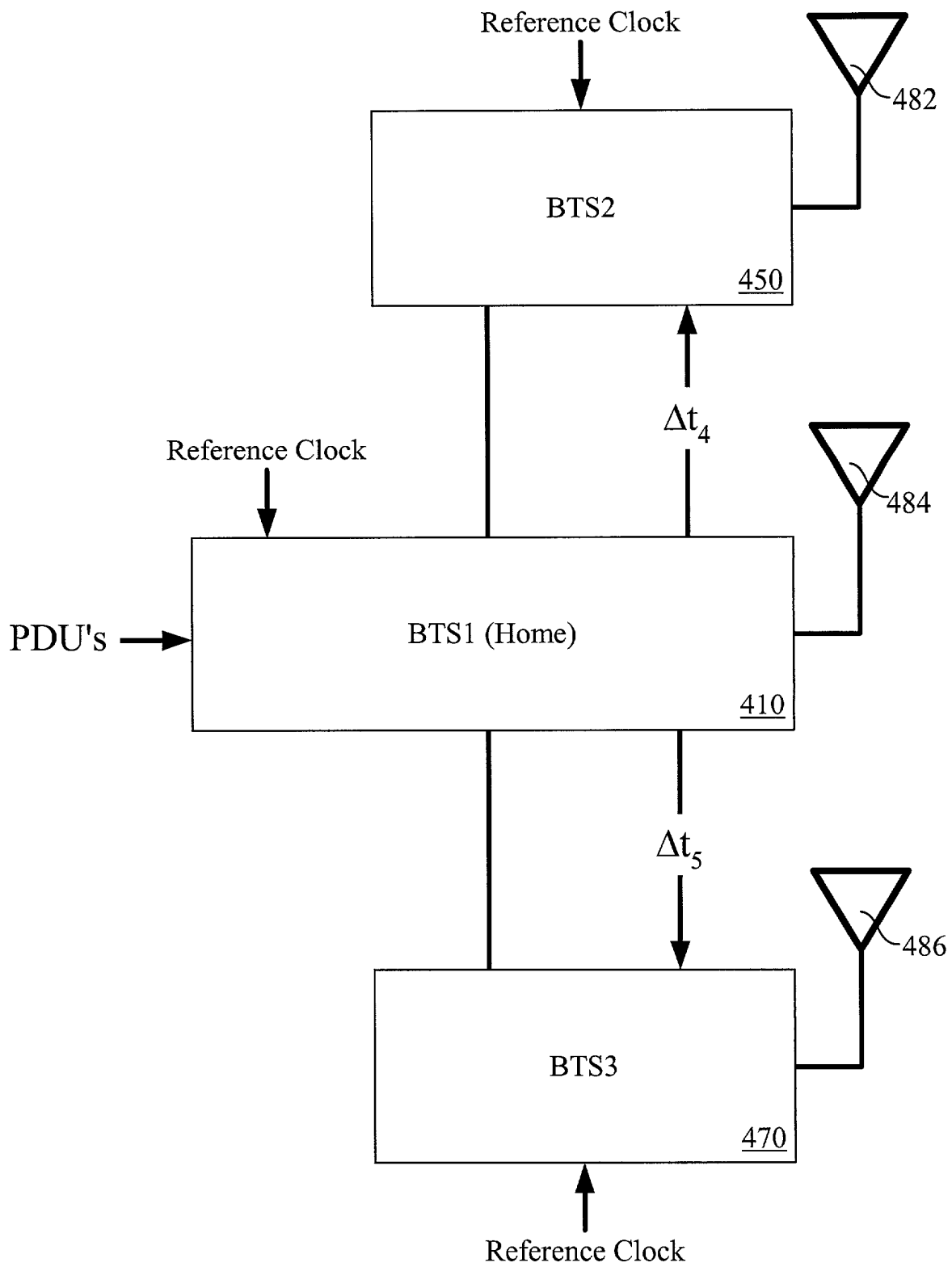


Fig. 6

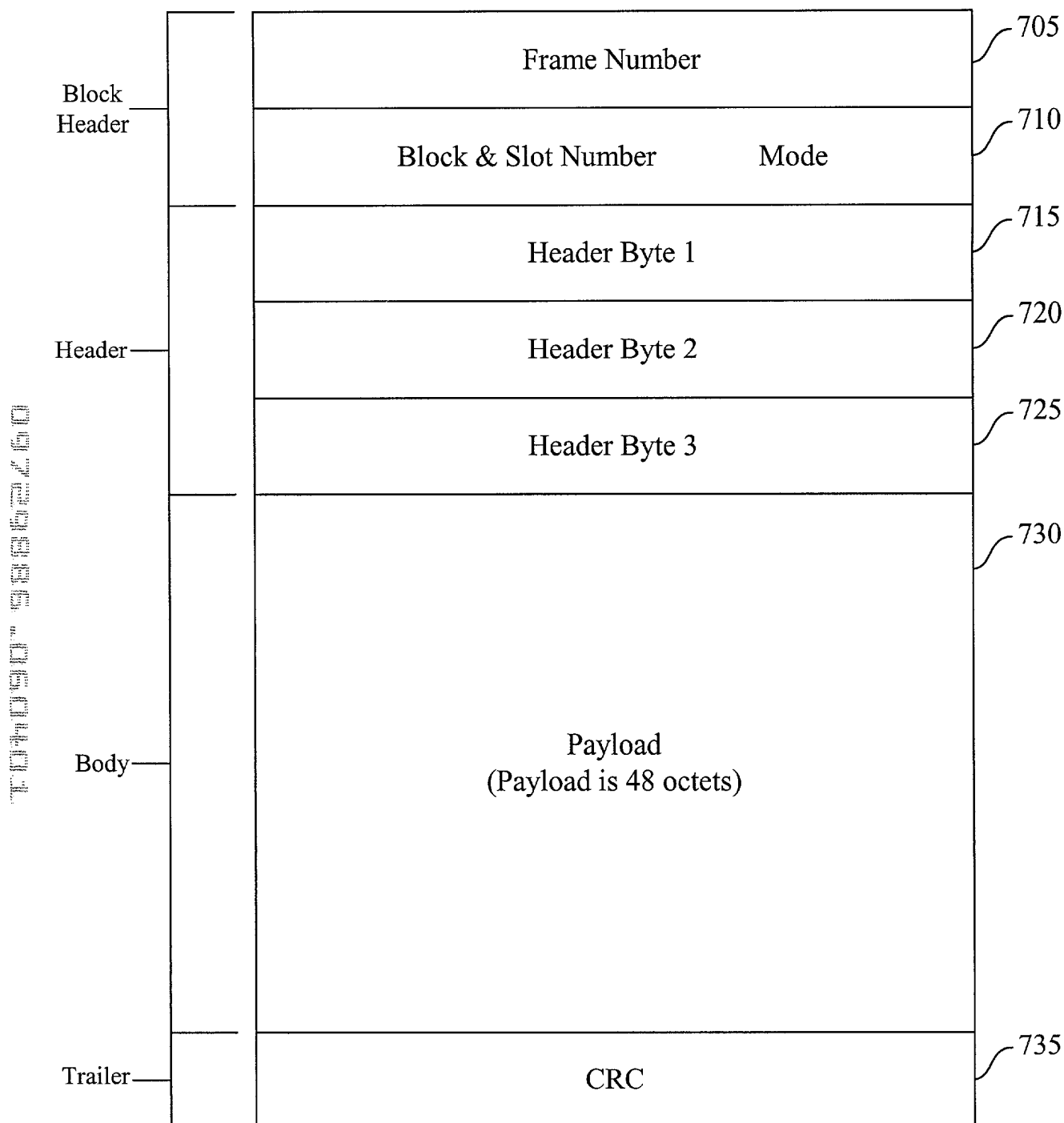


Fig. 7

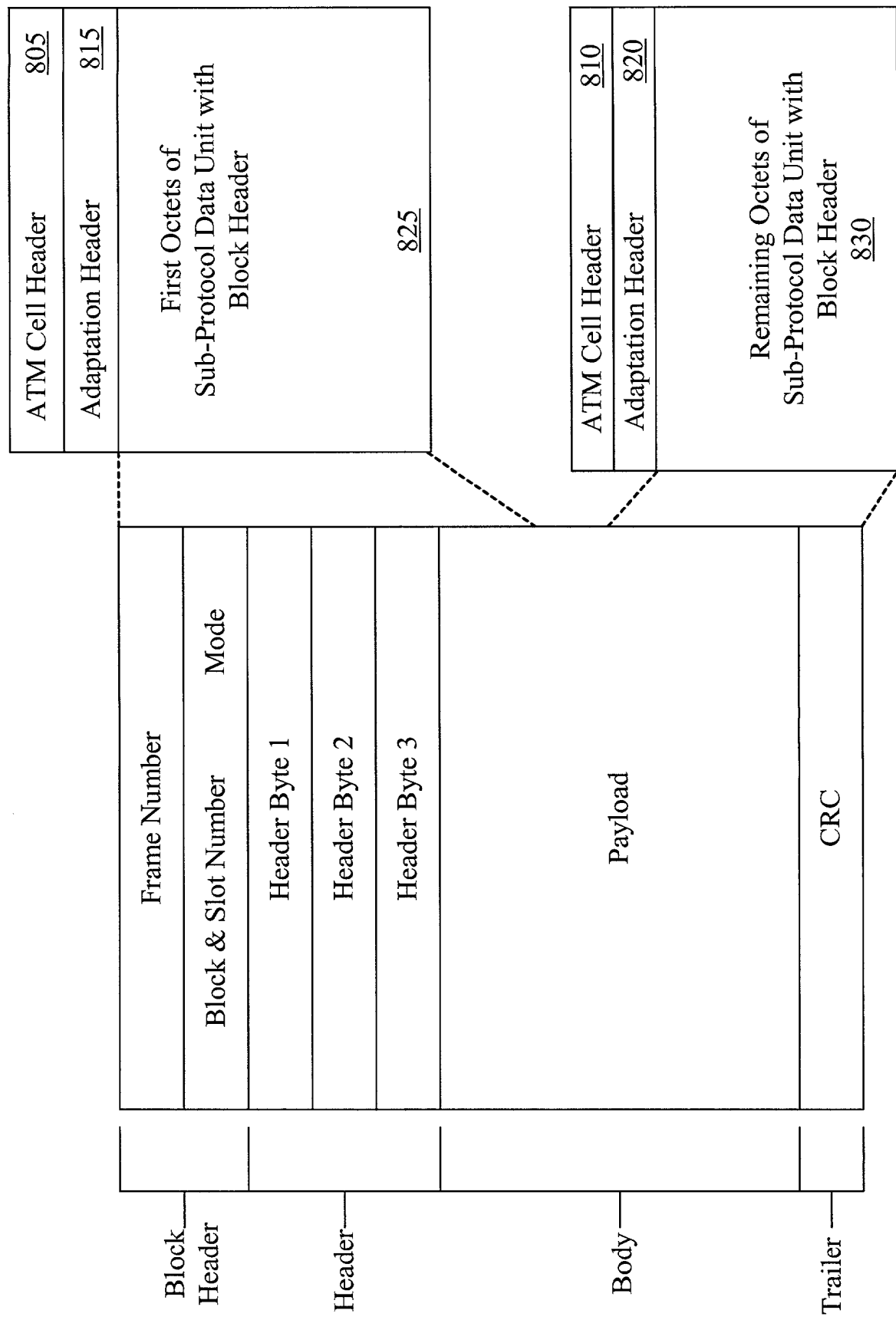


Fig. 8

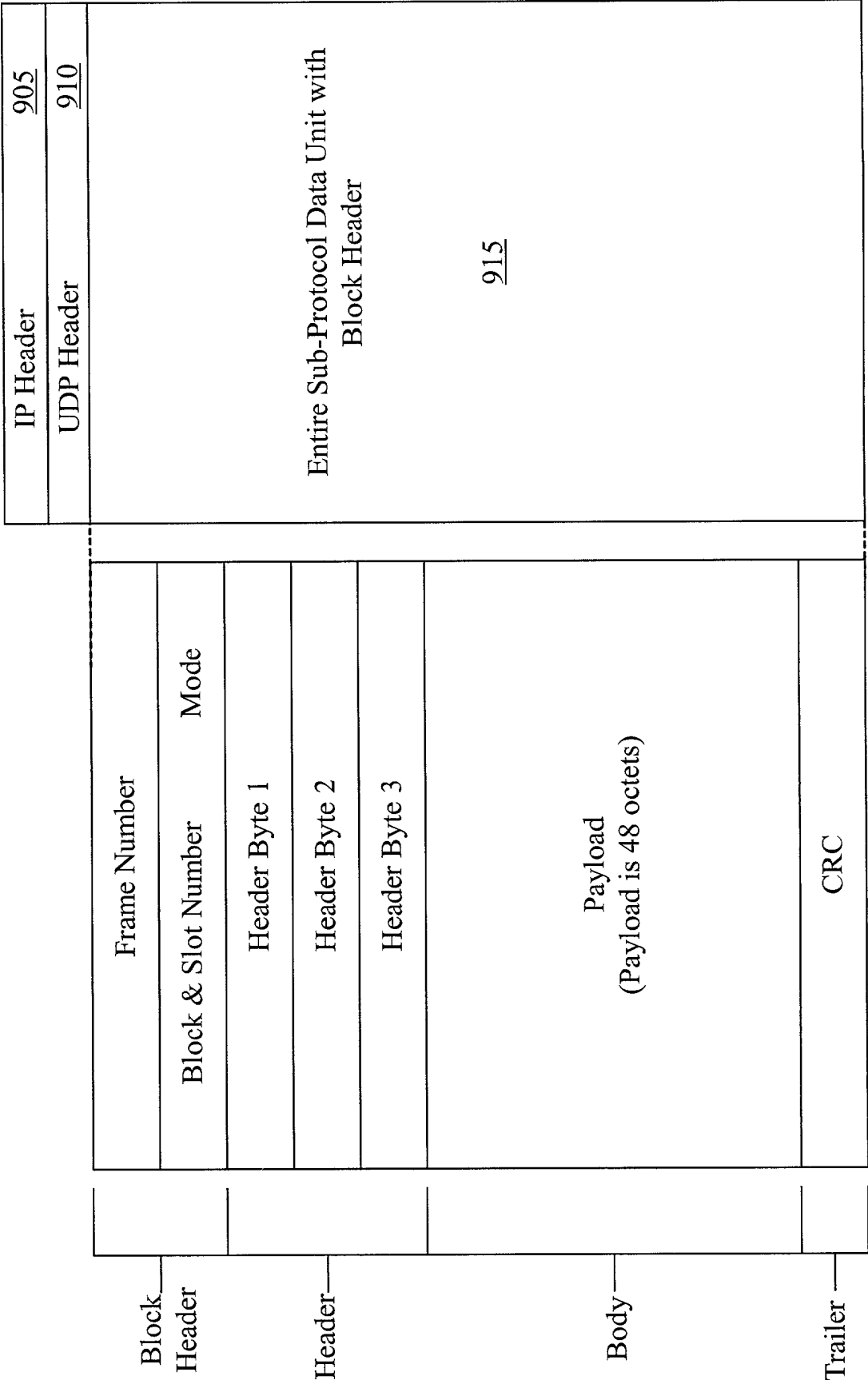


Fig. 9

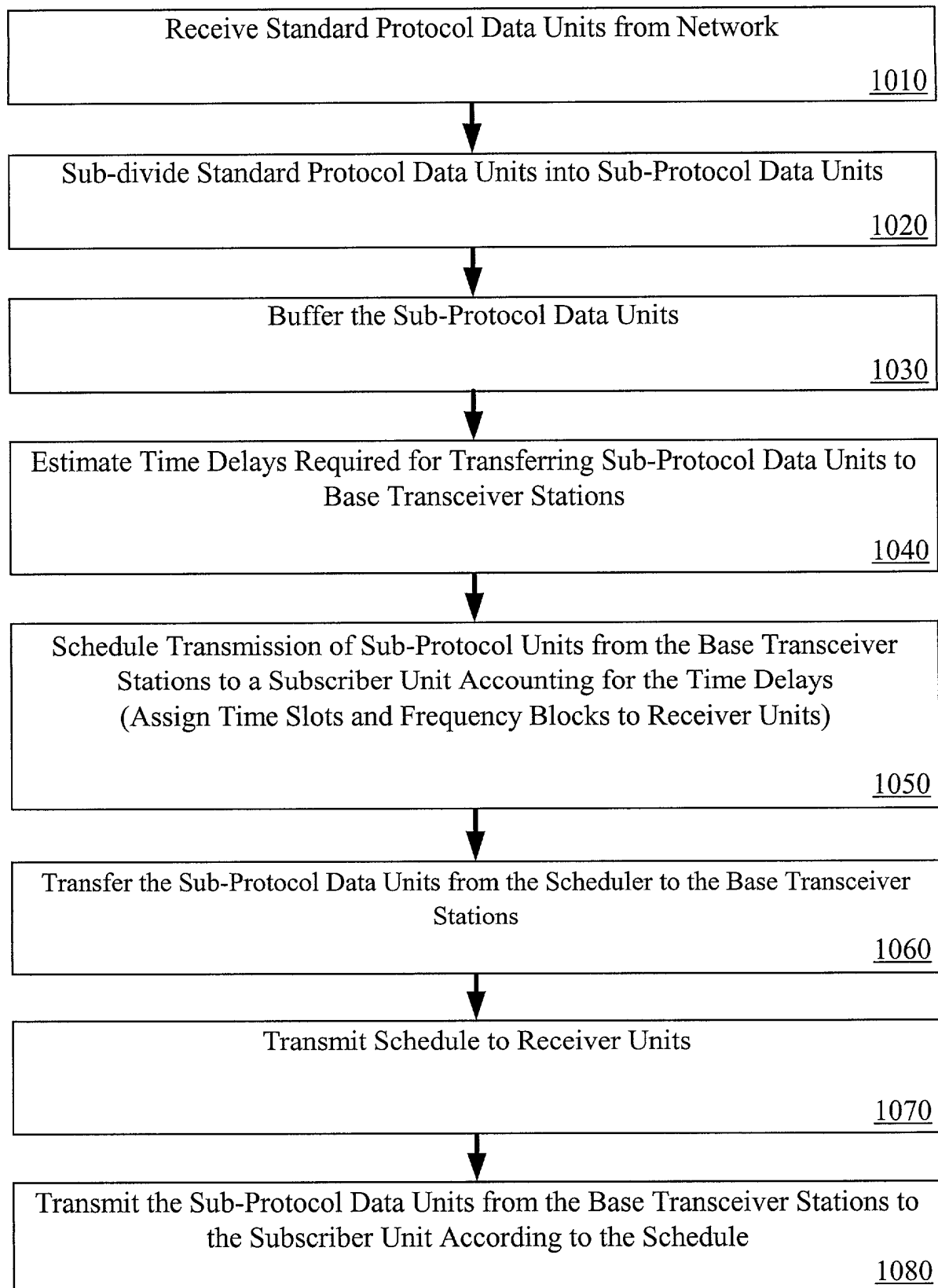


Fig. 10A

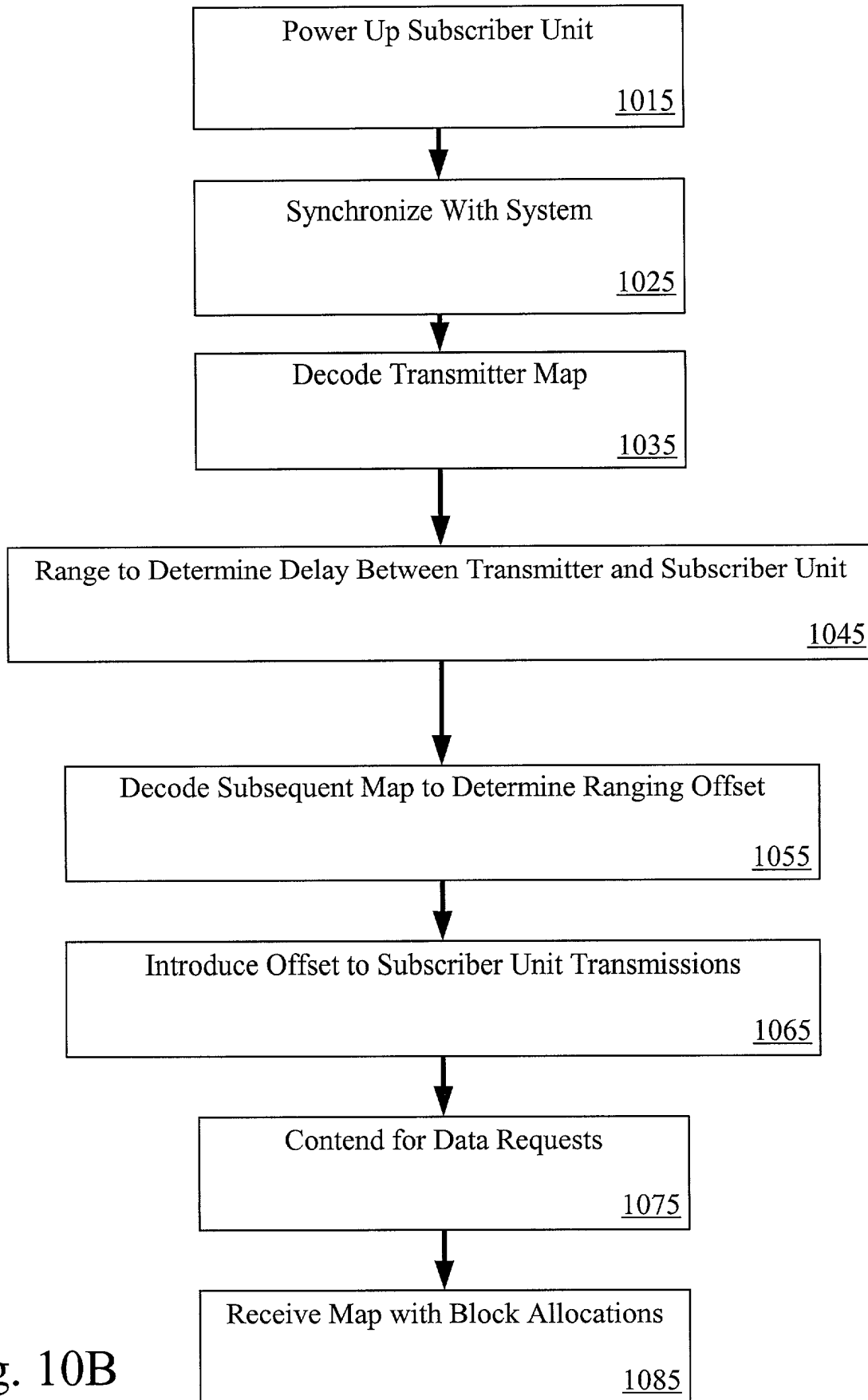


Fig. 10B

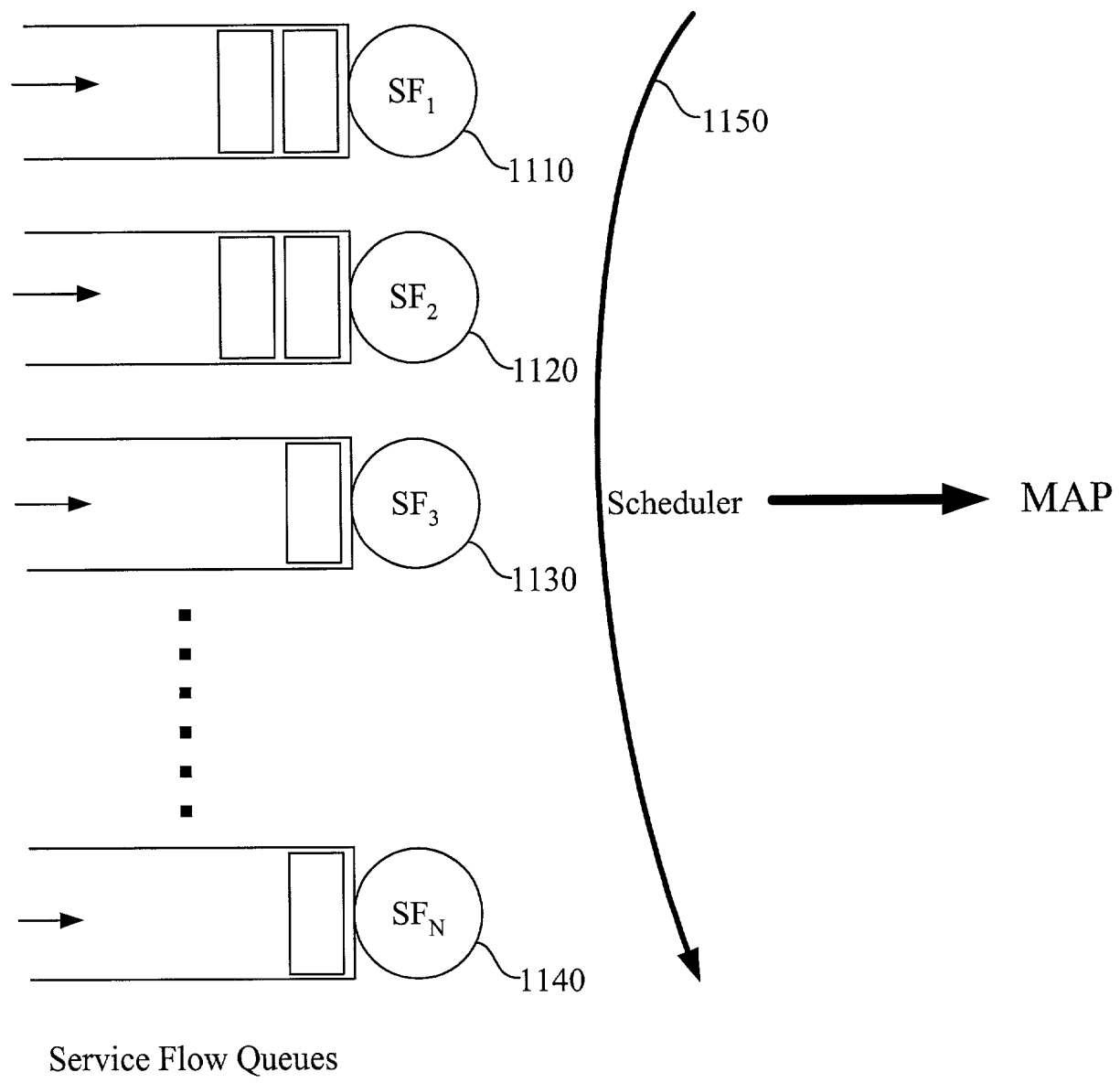


Fig. 11A

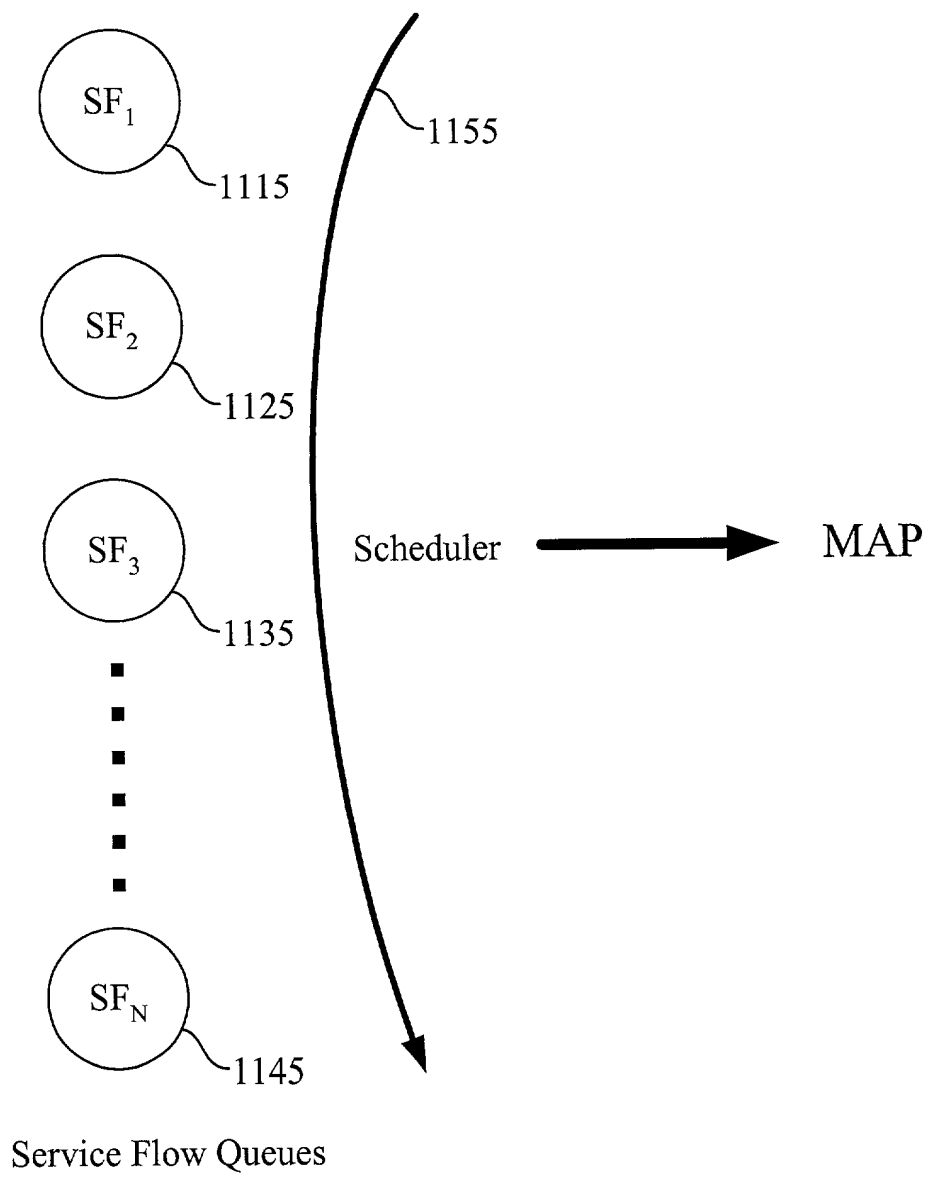


Fig. 11B

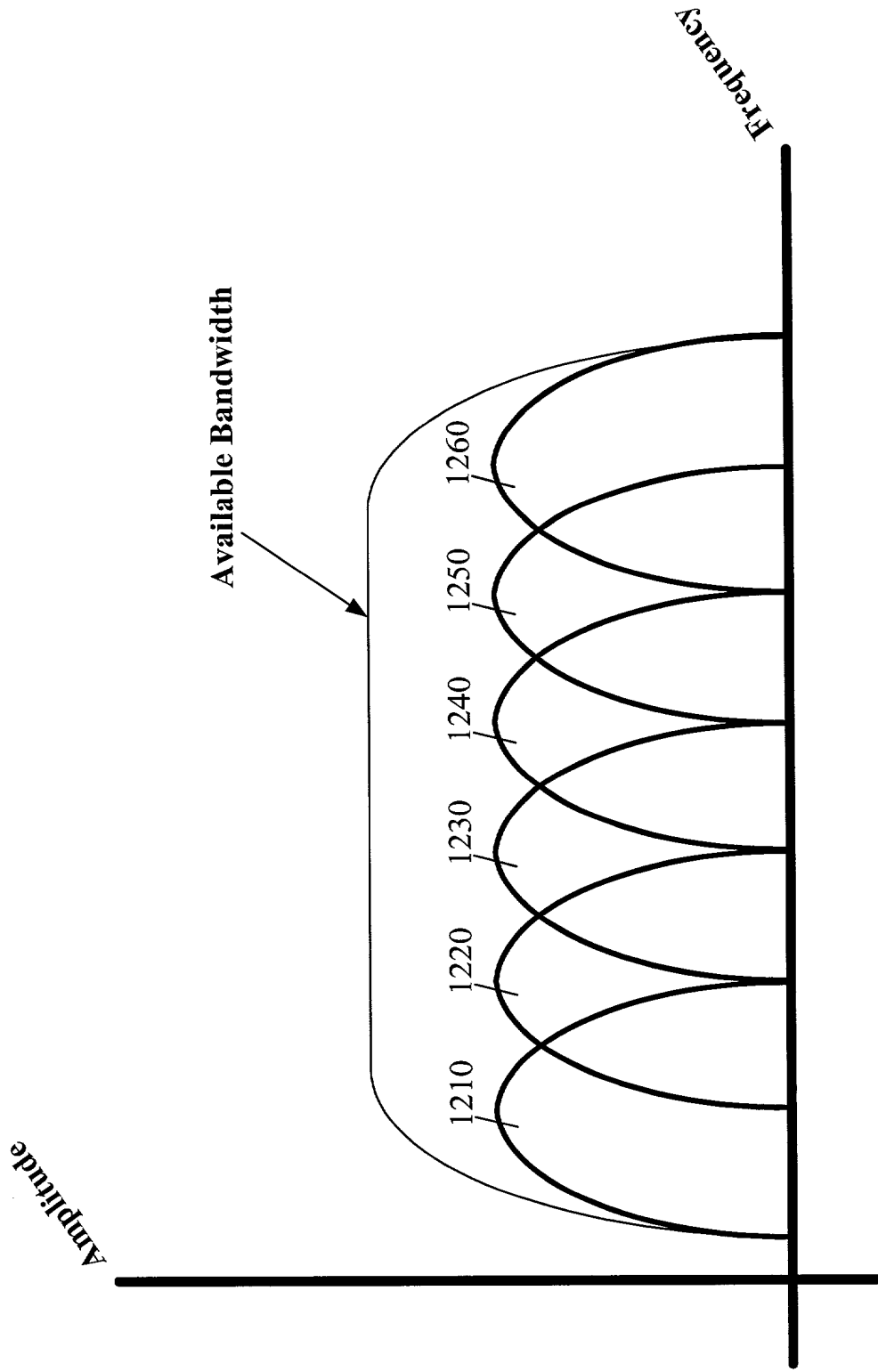


Fig. 12

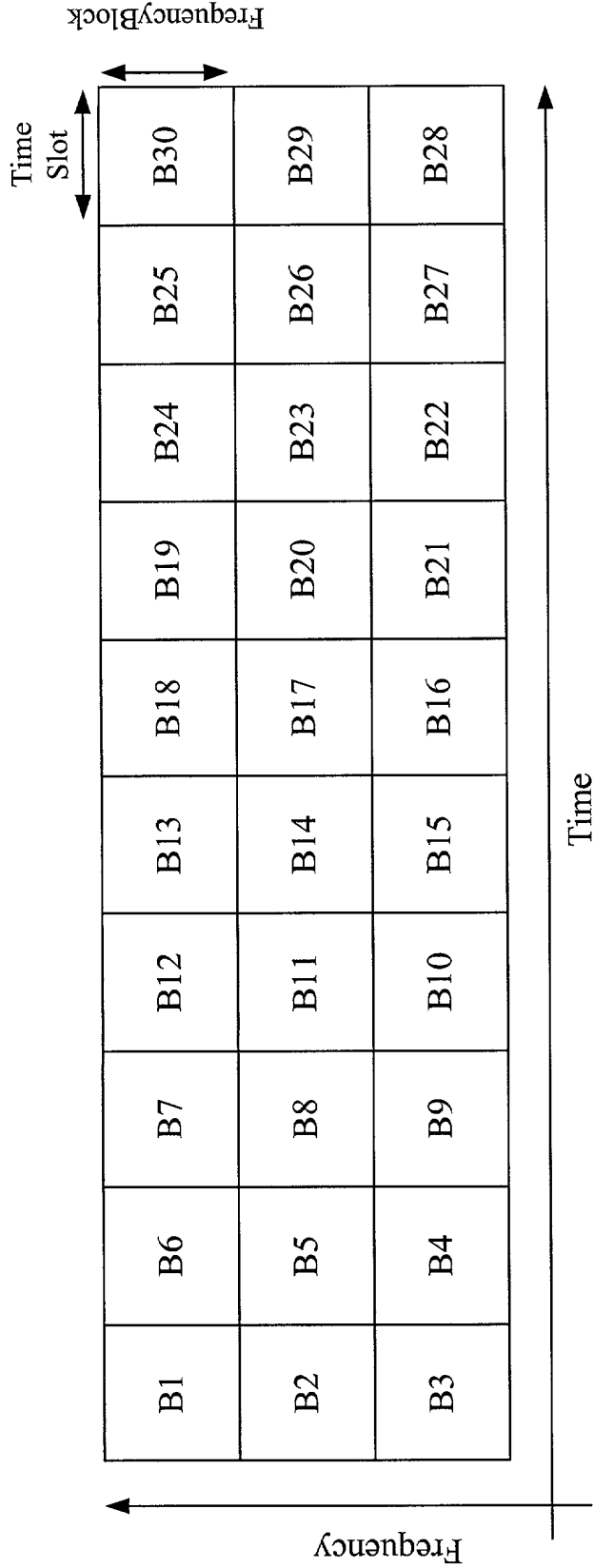


Fig. 13A

SF ID	SF Queue Size	Mode Assignment	Block Weight	System Mode
SF ₁	SFQ ₁	M ₁	BW ₁	SM
SF ₂	SFQ ₂	M ₂	BW ₂	DIV
SF ₃	SFQ ₃	M ₃	BW ₃	Nothing
SF _N	SFQ _N	M _N	BW _N	DIV

Fig. 14

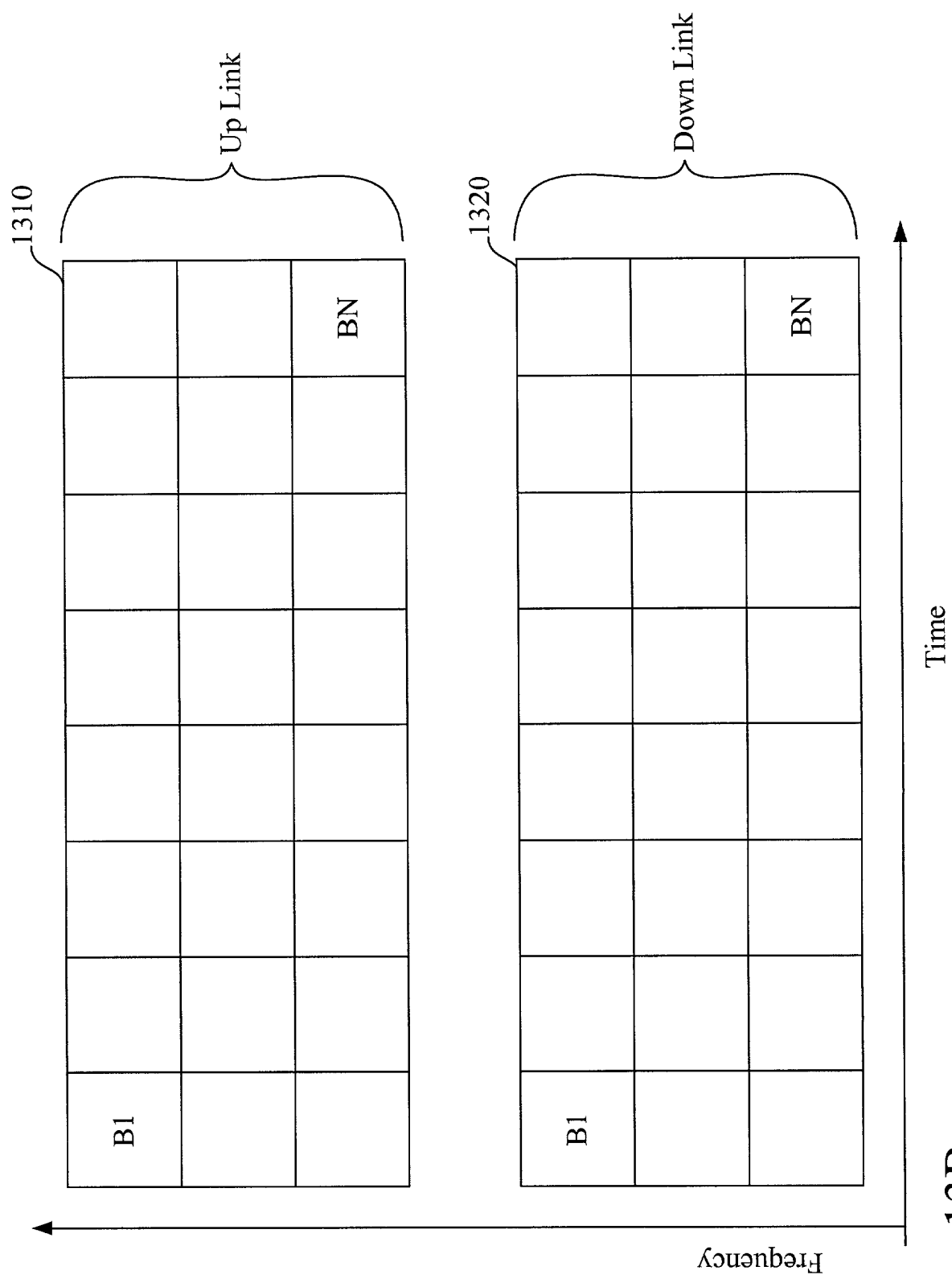


Fig. 13B

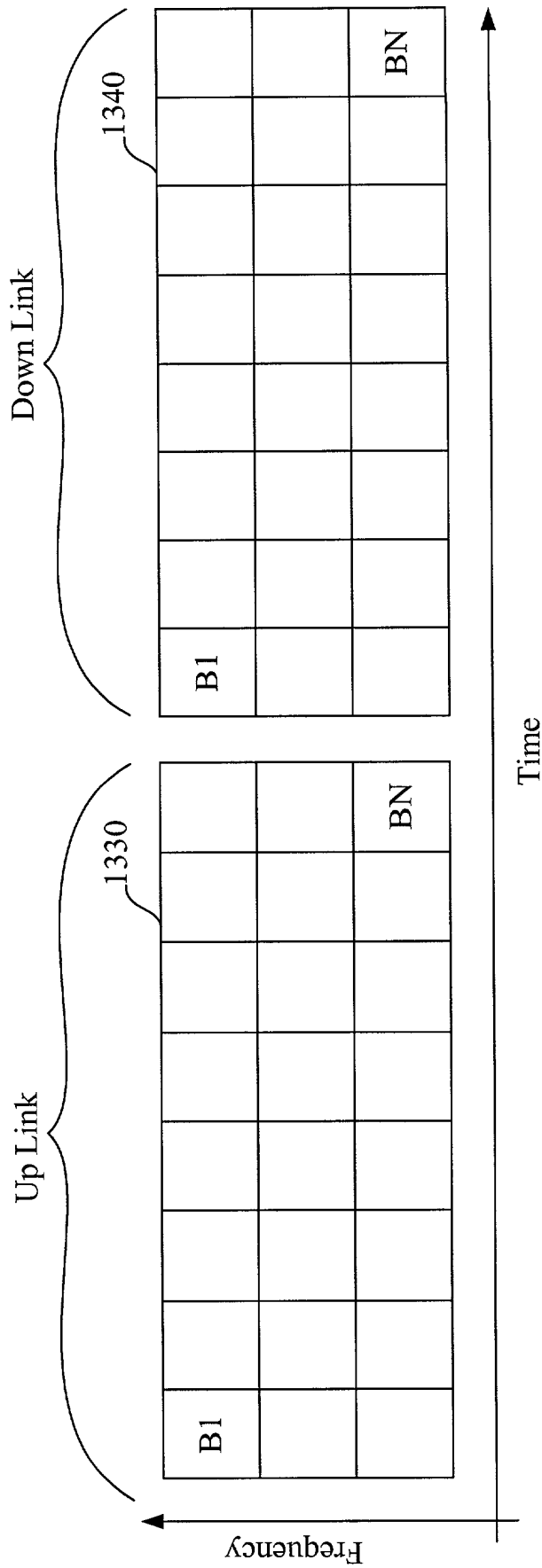


Fig. 13C

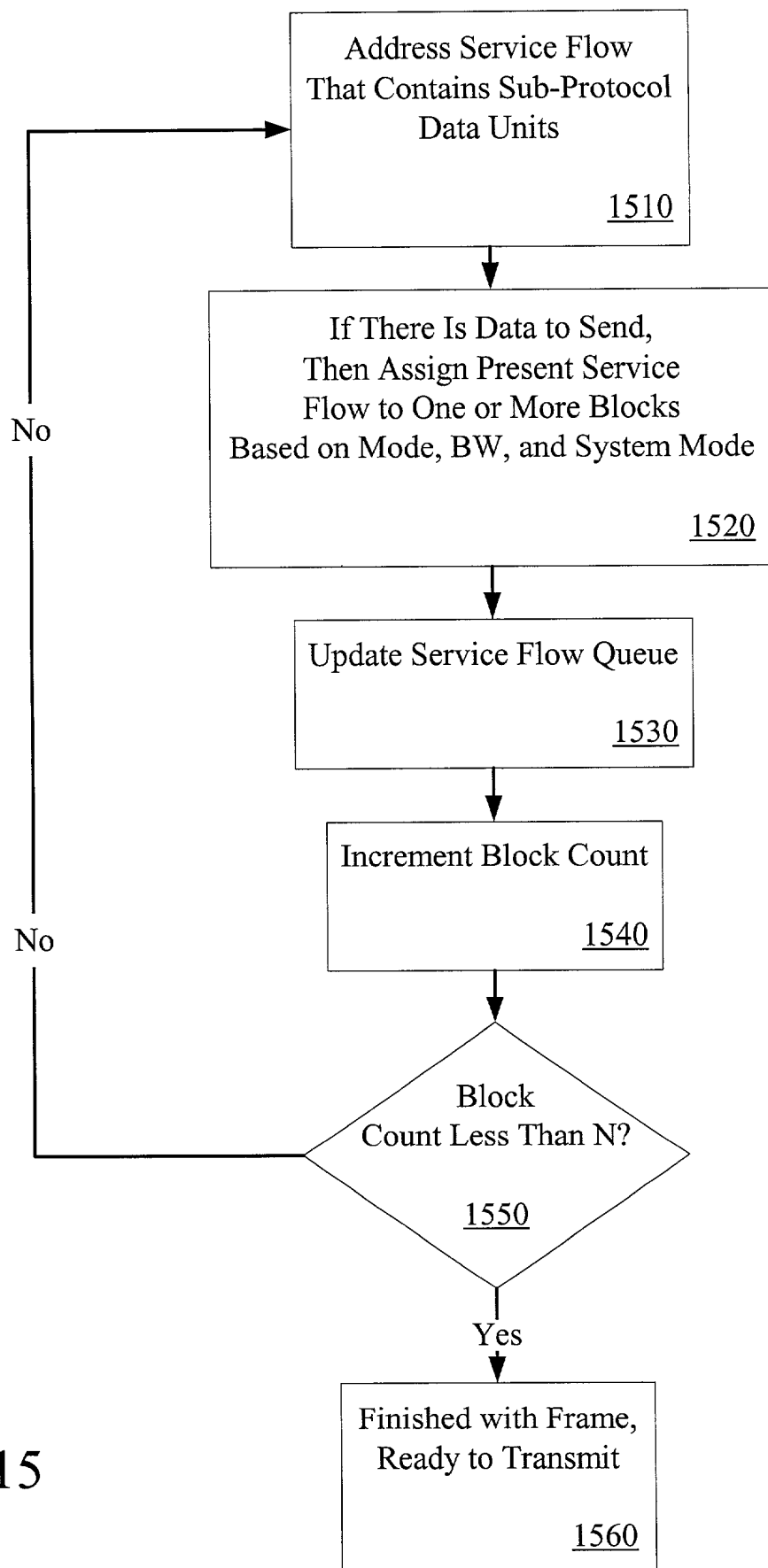


Fig. 15

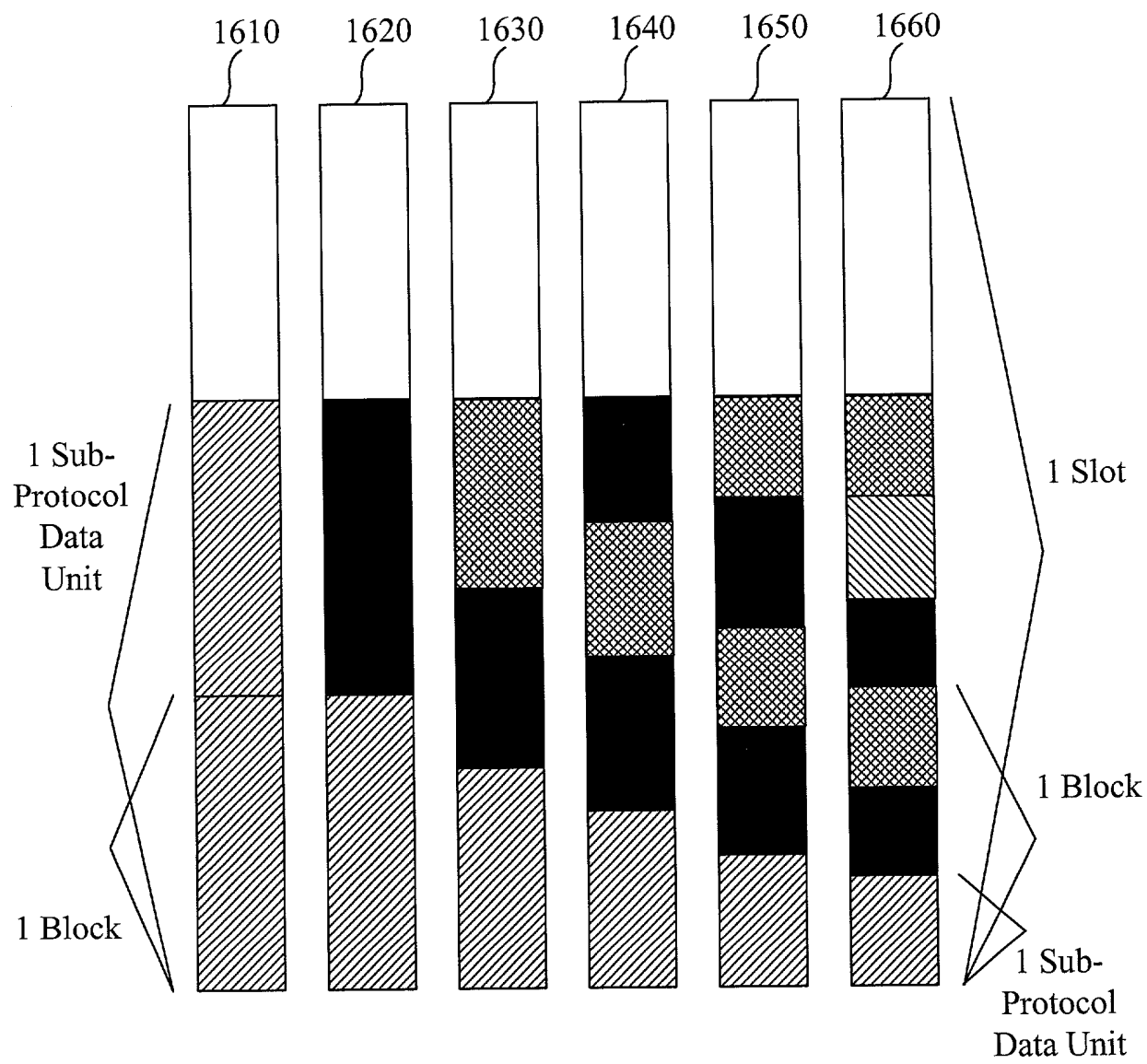


Fig. 16

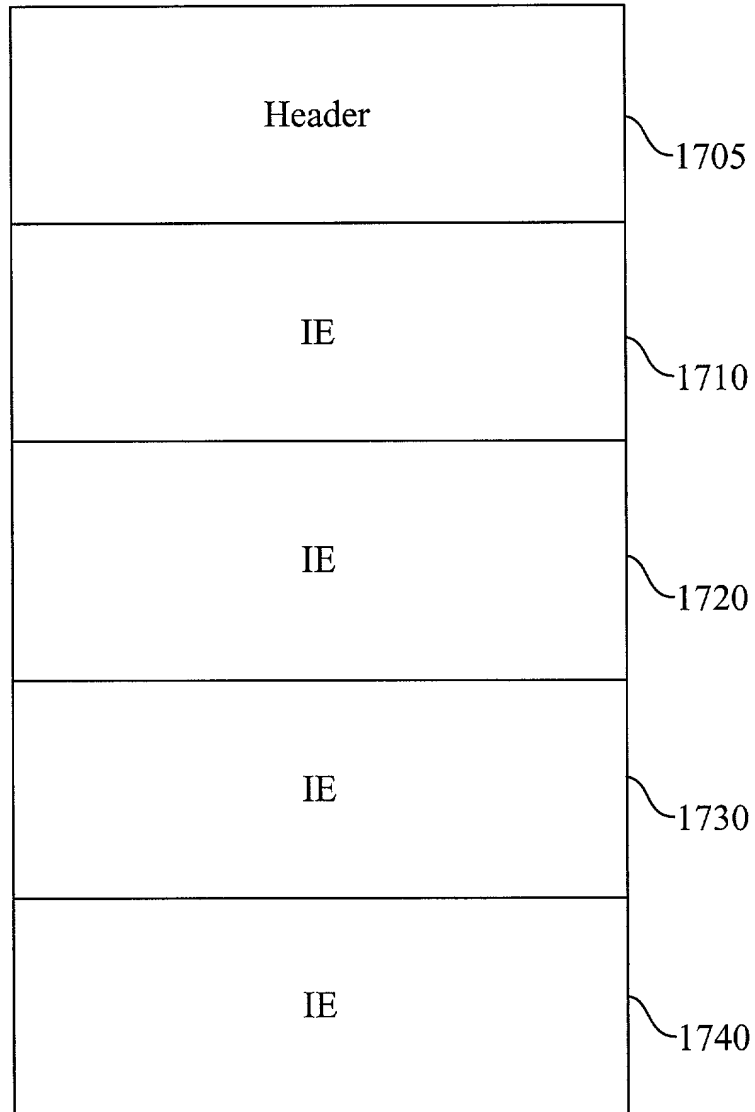


Fig. 17